Hogan & Hartson 81784.0285 Masato TERATINI et al. Image Data Processor and Image... EV 325 217 307 US 7 Drawing Sheets; Sheet 1 of 7 DRAFT-DESIGN MODE SELECTION SIGNAL AMPLIFIER SIGNAL GENERATOR CIRCUIT GENERATOR BRIGHTNESS COLOR DRAFT-DESIGN MODE SELECTION SIGNAL 22 SELECTOR œ COMPLE-MENTING CIRCUIT DIGIT-PROCESSOR CIRCUIT DATA HISTOGRAM CIRCUIT BIT EXTRACTOR CIRCUIT SIGNIFICANT MEMORY A/D CON-VERTER CIRCUIT

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COLUMN 1 COLUMN 2 COLUMN 3 COLUMN 4 COLUMN 5 COLUMN 6 COLUMN 7 COLUMN 8 COLUMN 9	α	ŋ	α	Ŋ	α	D.	TARGET PIXEL	
	ŋ	В	ŋ	ю	ŋ	В		
	α	8	œ	8	ď	g		
	5	В	ŋ	ш	5	В		
	ч	8	ď	19	R	6		
	5	В	Ŋ	В	9	В		
	œ	8	œ	8	œ	G		
	9	В	g	В	g	В	i	
	œ	ŋ	œ	5	Я	g		
	ROW a	ROW b	ROW c	ROW d	Row e	ROW f		

<u>1</u>8. 2

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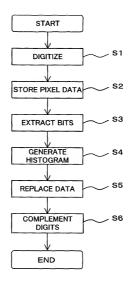
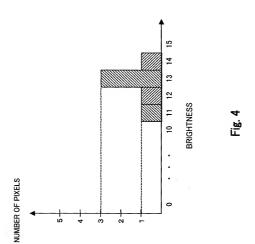
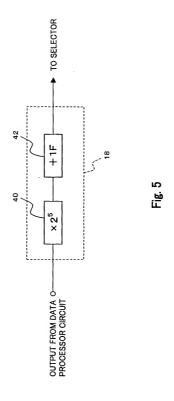


Fig. 3

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Fig. 6

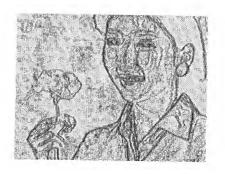
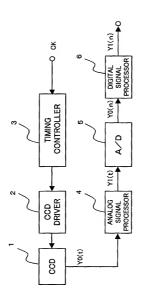


Fig. 7

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